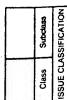
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APPLICATION NO.	CONT/PRIOR	CLASS .	SUBCLASS	ART UNIT	EXAMINER
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Koji Furusawa

Semiconductor device

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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)			
The term of this patent shall not extend beyond the expiration date of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
	(Primary Examiner) (Date)					
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this patent have been disclaimed.	(Legal Instrum	nents Examiner)	(Date)			

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